

PATENT

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**ABSTRACT OF THE DISCLOSURE**

A flipflop having a clock input for applying a clock signal, a data input for applying a data signal, a noninverting output and an inverting output, where the flipflop has a first holding element with a first feedback loop and a second holding element with a second feedback loop, where the first and second feedback loops each have a first node and a second node, where the first holding element is designed such that upon a first clock edge of the clock signal the logic value of the data signal is transferred to the first holding element and the logic value of the data signal is made available on the first node in the first feedback loop, where the first node in the first feedback loop is coupled to the first node in the second feedback loop in order to transfer the signal value which is on the first node in the first feedback loop to the second holding element upon a second clock edge of the clock signal and to output the signal value on the noninverting output, wherein the second node in the first feedback loop is coupled to the second node in the second feedback loop in order to transfer the inverted signal value which is on the second node in the first feedback loop to the second holding element upon the second clock edge of the clock signal, where the second node in the second feedback loop corresponds to the noninverting output and the first node in the second feedback loop corresponds to the inverting output.

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